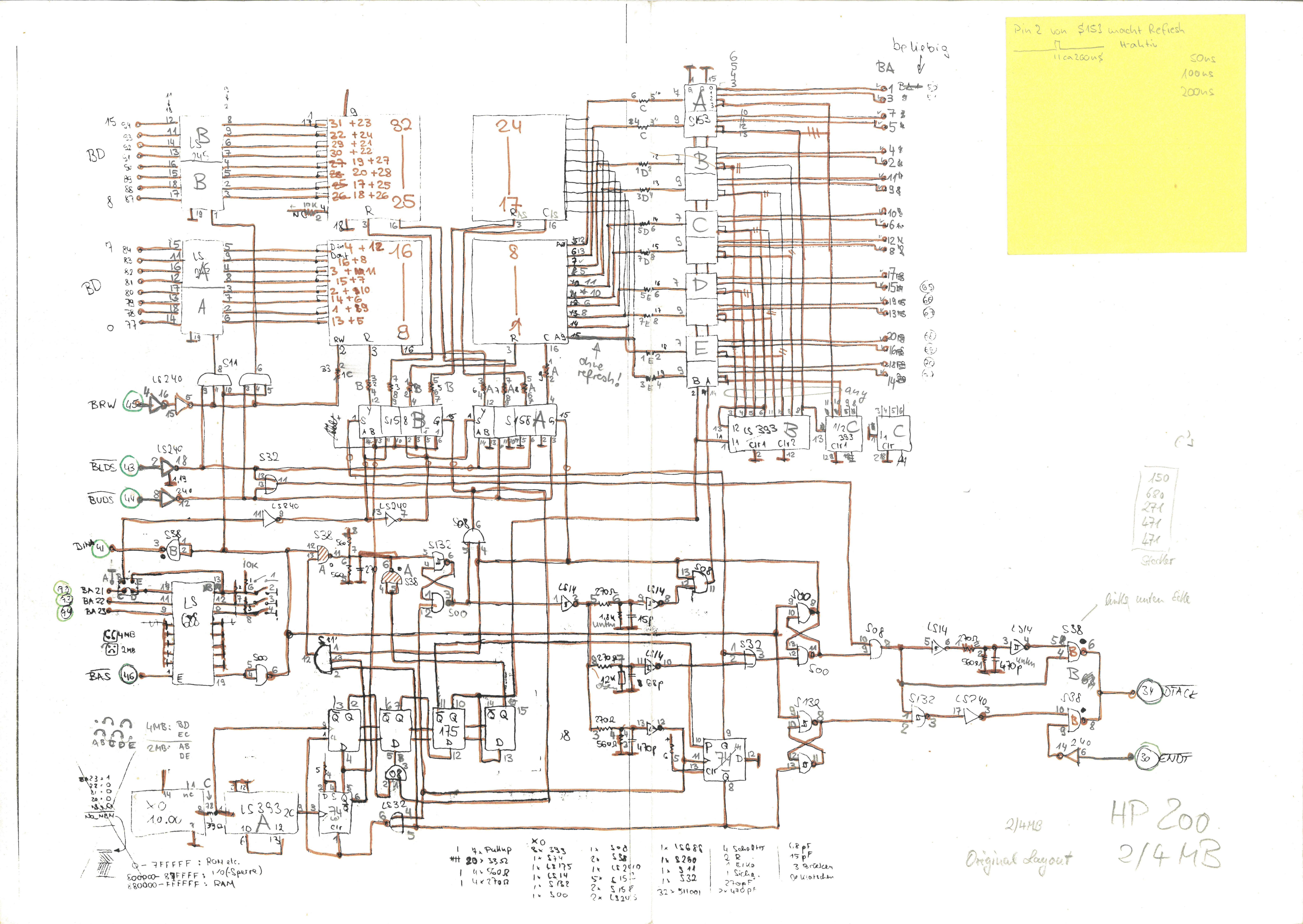
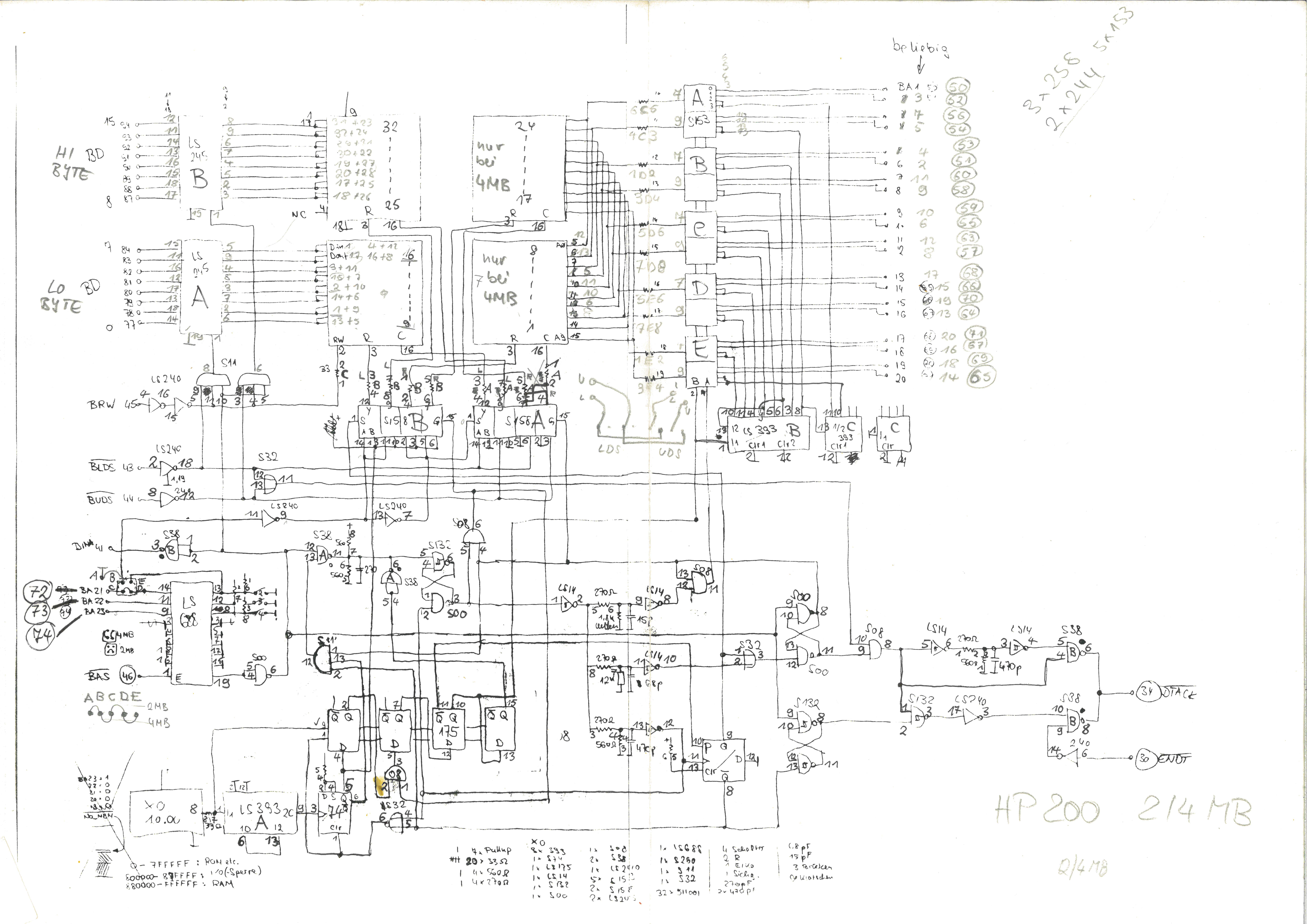


Pin 2 von S153 macht Refresh
H-aktiv
ca 200 ns
50ns
100ns
200ns
beliebig
BA
BD
BRW
BLDS
BUDS
DIN
BAS
DTACK
ENDT
ohne refresh!
links unten Ecke
Stecker
4MB: BD EC
2MB: AB DE
0 - 7FFFFF : ROM etc.
800000 - 87FFFF : I/O (-Sperre)
880000 - FFFFFF : RAM
2/4MB
Original Layout
HP 200
2/4 MB



beliebig
3x258
2x244
5x153
HI BD BYTE
LO BD BYTE
LS 245 B
LS 245 A
nur bei 4MB
nur bei 4MB
NC
LDS
UDS
LS 393 B
BRW
BLDS
BUDS
LS240
S11
S32
S38
S00
S08
S132
LS14
LS 688
BAS
ABCDE
2MB
4MB
175
XO 10.00
LS393 A
74
DTACK
ENDT
0 - 7FFFFF : ROM etc.
800000 - 87FFFF : I/O (-Sperre)
880000 - FFFFFF : RAM
HP 200 2/4 MB
0/4 MB